

KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE Opp: Yerragattu Gutta, Hasanparthy (Mandal), WARANGAL - 506 015, Telangana, INDIA. काकतीय प्रैद्योगिकी एवं विज्ञान संस्थान, वरंगल - ५०६ ०१५ तेलंगाना, भारत కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - గండ రంగా కెలంగాణ, భారతడేశము

(An Autonomous Institute under Kakatiya University, Warangal)

(Approved by AICTE, New Delhi; Recognised by UGC under 2(f) & 12(B); Sponsored by EKASILA EDUCATION SOCIETY)

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING CLASSWORK TIMETABLE for the ODD SEMESTER of Academic Year 2022-23

III SEMESTER of M. Tech (POWER ELECTRONICS)

Class Teacher Dr. V. Srikanth 15.10-16.00-9.10-10.00-10.50-11.40-14.20-13.30-Day 16.00 16.50 14.20 15.10 10.00 10.50 11.40 12.30 DP-I / IP IS LIBRARY Monday **SEG** SEG (T) LUNCH BREAK IS IS SEG SEG DP-I / IP Tuesday DP-I / IP DP-I / IP Wednesday DP-I / IP DP-I / IP Thursday Friday Saturday

Course Title		Course Code & Course Name		Course Faculty
SEG	1:	P20PE301 A Smart Electric Grid (SH 3-2)	-	Sri D. Sarath
IS	:	P20OE302B Industrial Safety (SH 2-6)	-	Ms. K. Manasa
DP-I/IP	:	P20PE303 Dissertation Phase-I / Industrial Project	-	Dr. V. Srikanth

Dept. Timetables

Faculty I/c, **Overall Timetables**

Head of the Department